

## Curriculum Vitae

**Name:** Mrs. Salunke Vaishali Sachin

**Designation:** Asst. Prof., Department of Electronics, Modern College  
Of Arts, Science and Commerce , Ganeshkhind,Pune-411016

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### **Academic Qualifications :**

Exam. Passed	Year of Passing	Board/University	% of Marks Obtained
S.S.C.	1994	Maharashtra State	85.58
H.S.C.	1996	Maharashtra State	77.83
B.Sc.	1999	Pune	70.30
M.Sc.	2001	Pune	57.38

### ➤ **Other Qualifications:**

- Certificate Course in **Vedic Mathematics**.
- Certificate course in **Embedded Systems** held at Govt. College Of Engineering Pune .
- Training Program in **Raspberry Pi** held at Department of Electronics , D.Y.Patil College of Engineering, Akurdi, Pune.
- 4 months **Industrial Training** at Forbes Marshall, Kasarwadi, Pune

### ➤ **Software Proficiency:**

- **Assembly Language Programming** for various **Architectures** used :
- **C** and **Embedded 'C'** language Programming
- Keil -7, MPLAB-7, NASM-7

- MATLAB-7
- Python2.8
- PCB design software: EAGLE-3, EXPRESS PCB-7

➤ **Architectures used :**

- 8085,Pentium Microprocessors
  - 8051,Microchip PIC,ARM-7 Microcontrollers
  - Raspberry Pi
  - Arduino platform
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➤ **Professional Information :**

➤ Cleared SET examination in 2002

➤ Teaching Experience: 20 years as on 1<sup>st</sup> July 2021

- Modern College of Arts ,Science and Commerce, Ganeshkhind (2003 till date )
- Tuljaram Chaturchand College, Baramati (2001-02)
- Sysbiosis International University ,S.B.Road,Pune(2002-03)

➤ **Subjects Taught:**

- Principals/ Fundamentals of Analog Electronics
- Principles/Fundamentals of Digital Electronics ,
- Embedded System Design
- Analog and Digital System Design
- Communication Principles
- Computer Organisation and Microprocessor Architecture
- 8051 programming and Embedded Systems
- 8051 Architecture, programming ,and Interfacing
- Operational Amplifiers

➤ **New methods introduced in teaching:**

- **Use of coloured chalks** for interconnections of the circuit to ease the understanding of circuit connections.
  - **One minute games** to improve basic technical skills in Electronics.
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➤ **Career Advancement Activities:**

- Completed one **Orientation program** and one **Refresher course** in Electronics.
- Course in **Research Methodology and Publication Ethics** conducted by Abasaheb Garware College of Science, Paud Rd. Pune

**Research Project:** Minor Research Project Vigilant Attendance System using fingerprints and GSM based feedback sanctioned by BCUD, SPPU, Pune

➤ **Co-curricular activities :**

**Examination Duties:**

**External Senior Supervisor:**

- Pune Vidhyarthi Gruh, Science college,Pune.
- Sinhagad Business School, Erandwane, Pune,
- Dr. Babasaheb Ambedkar College, Aundh, Pune October 2019 examination s

**Paper Setting:**

- F. Y. B. Sc. Comp Sc. 2013 pattern and CBCS 2019 Pattern [ April 2020, October 2020, April 2021 SPPU Examinations ]

**Examiner :**

- F.Y.B.Sc. Electronics and Computer Science.

- S.Y.B.Sc. Computer Science
- T.Y.B.Sc. Electronics

### **Syllabus Design:**

- UGC sanctioned Career Oriented Course in Embedded Systems and Applications under XIth Plan .
- Hands on Training under DBT Star college Scheme and skill development courses on PCB design and Photovoltaic System design and Installation at own department.
- Curriculum enrichment course of 30 hrs. on “ Internet of Things using Raspberry Pi”
- Extra credit course on “Fundamentals of Solar Energy and Photovoltaic System Installation ” at college level .

### **➤ Administrative Roles:**

- Working as a **Head, Department of Electronics** of the college from academic year 2017-18.
- Chairperson for **Science Association** from Academic Year 2017-18
- Chairperson **College Prospectus Committee** during academic year 2012-13 to 2016-17
- Coordinator for **Practical Journal Committee** during 2013-18
- Member of criteria I during 3<sup>rd</sup> assessment cycle and Criteria IV during 4<sup>th</sup> assessment cycle .
- Member of F.Y.B.Sc. Comp Sc. **Admission committee** 2006-2009 and 2018-19 onwards
- Member of **Internal Examination Committee**

➤ **Activities Organized in the capacity of Chairperson:**

- Hands on Training **“Printed circuit board design”** using Eagle software in 2013-14 and using EXPRESS PCB-7 in 2014-15 under DBT STAR scheme funds.
- Two days Workshop **“Use of Solar Energy ”** in 2015-16 under DBT STAR scheme funds.
- **Indian Science Academies’ Two days Short duration Lecture workshop on “Smart Materials for sustainable development”** held on 17<sup>th</sup> and 18<sup>th</sup> January 2018.
- Two days **workshop on “Interfacing sensors to Arduino Uno”** in the month of January 2018 sanctioned under activities supported by Student’s Development Board of Savitribai Phule Pune University.
- **“Anubhuti”, Regional Science Project and Poster competition** organised on the occasion of Women’s Day with theme **“Science Technology and Innovations for her-Health, Security and Comfort”** on 2<sup>nd</sup> March 2019 **in association with Sharada Shakti** : Pune based National organization working for woman empowerment and Science Popularization.
- Two days **workshop on “Develop practical approach on use of Solar Energy”** in February 2019 sanctioned under activities supported by Student’s Development Board of Savitribai Phule Pune University.
- Designed and implemented **Curriculum enrichment course of 30 hrs. on “ Internet of Things using Raspberry Pi”** in the month of June 2019.
- **“Anubhuti”, Regional Science Project and Poster competition** organised on the occasion of Science Day celebration on 5<sup>th</sup> February 2020 sanctioned **under DBT-CTEP scheme of Exhibitions** of Department of Biotechnology, Govt. of India.

- **“E-Techno fair”** various competitions based on fundamental knowledge and basic technical skills in Electronics held on 8<sup>th</sup> February 2020 sanctioned under activities supported by Student’s Development Board of Savitribai Phule Pune University.
- **“Anubhuti” , National Science Competitions** organised on the occasion of Science Day celebration on 2<sup>nd</sup> March 2021 sanctioned under DBT Star Status funds.

➤ **Organising Committee Member:**

- One day state level workshop on **Role of Advanced Sensors in the development of Smart City.**
- **“Avishkar” Zonal research conference** organised by Savitribai Phule Pune University in association with Modern College ,Ganeshkhind.
- Local workshop on **Lab Saftey** for Laboratory Staff from various colleges in Pune city.
- Departmental Coordinator for various activities under DBT STAR college scheme.
- Coordinator for **UGC COC Course** in Embedded Systems and Applications run by the department.

➤ **Worked as judge for competitions like:**

- Physics Quiz Competition held by Department of Physics Pune university and Modern College Ganeshkhind.
- Physics :Theme based Rangoli competition held at Physics department, Modern College ,Ganeshkhind.
- Intercollegiate Quiz Competition during “Interaction ” :an educational event held at Department of Computer Sc., Modern College, Ganeshkhind.

- Project assessment during “Anubhuti” Science Exhibition at Modern College, Ganeshkhind

➤ **Editorial Board Member for:**

- College Prospectus
  - Indradhanu: A Panorama of college Activities
  - Brochure :Skill Development Courses
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